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APPLICANTS

Keiji Hosotani, Tokyo, JAPAN;

** CONTINUING DATA *****

None. EP

** FOREIGN APPLICATIONS *****

JAPAN 2002-230126 08/07/2002

verified. EP

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 11/04/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 30	TOTAL CLAIMS 21	INDEPENDENT CLAIMS 7
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Verified and Acknowledged

Examiner's Signature *[Signature]* Initials *EP*

ADDRESS

22850
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TITLE

Semiconductor integrated circuit device and method of manufacturing the same

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RECEIVED 1284		<input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____

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